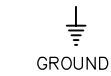




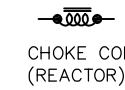
WATTMETER



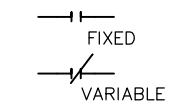
GROUND



FUSE

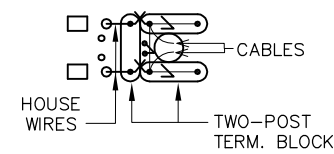


CHOKE COIL (REACTOR)

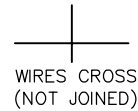


CONDENSER OR CAPACITOR (SHOW MFD VALUE)

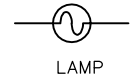
DETAILS FOR TERMINAL BOARD



TEST TERMINAL



WIRES CROSS (NOT JOINED)



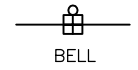
LAMP



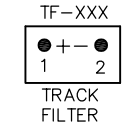
LIGHTNING ARRESTER



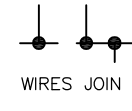
FIXED RESISTOR



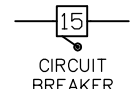
BELL



TRACK FILTER



WIRES JOIN



CIRCUIT BREAKER



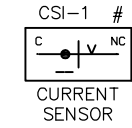
EQUALIZER



VARIABLE RESISTOR

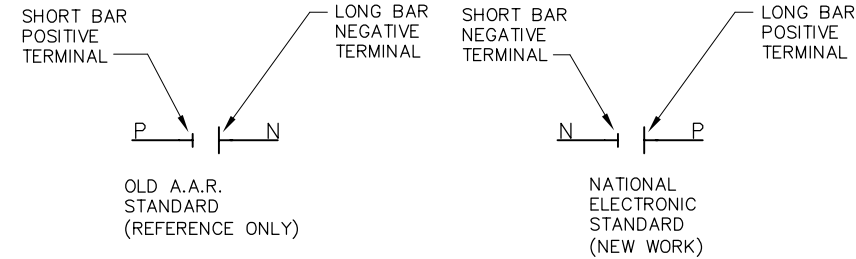


LED



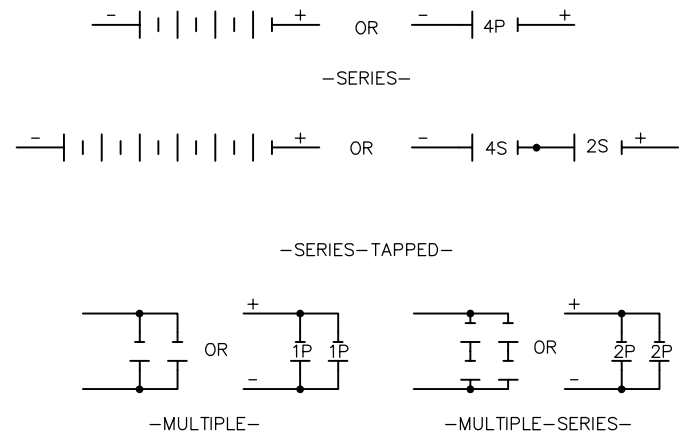
CURRENT SENSOR

BATTERIES



NOTE:
THE NATIONAL ELECTRONIC STANDARD IS SHOWN HERE TO ACQUAINT OUR PERSONNEL WITH THE SYMBOL USED BY OTHER INDUSTRIES AND IS NOT FOR USE ON SIGNAL CIRCUIT PLANS.

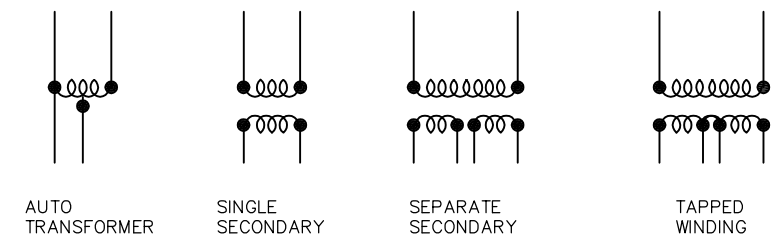
CIRCUIT ARRANGEMENTS



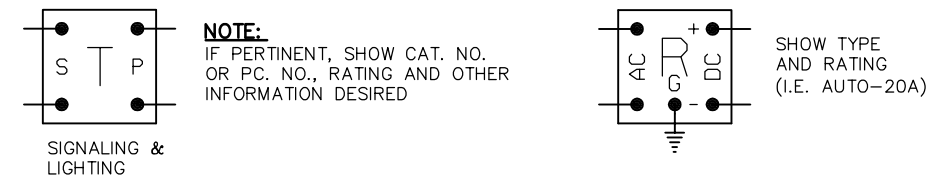
SPECIFY NUMBER OF CELLS USED AND USING SYMBOLS BELOW SHOW THE TYPE

D-DRY CELLS
N-NICKEL-IRON OR NICKEL-CADMIUM STORAGE CELLS

TRANSFORMERS



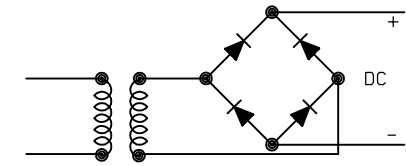
SIMPLIFIED SYMBOL



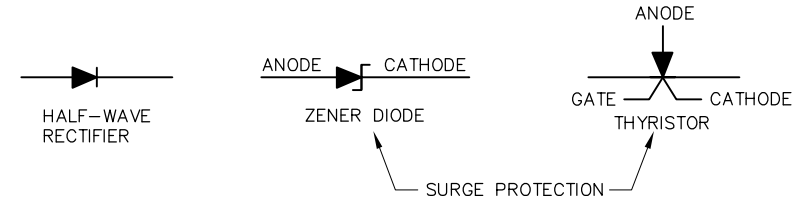
NOTE:
IF PERTINENT, SHOW CAT. NO. OR PC. NO., RATING AND OTHER INFORMATION DESIRED

SHOW TYPE AND RATING (I.E. AUTO-20A)

RECTIFIED (FULL WAVE)



DEVICES USED WITH RECTIFIERS



REV	DATE	BY	CHK	APP	DESCRIPTION

PENINSULA CORRIDOR JOINT POWERS BOARD

APPROVED BY:




ENGINEERING MANAGER DEPUTY DIRECTOR OF ENGINEERING



1250 San Carlos Avenue
 San Carlos, CA 94070

STANDARD DRAWINGS		CADD FILE NO.:
SIGNAL AND COMMUNICATION GENERAL SIGNAL COMPONENT SYMBOLS AND CIRCUIT PLAN SYMBOLS		SD-5102
REV	DATE	
0	093011	
SIGNAL		
STANDARD NO.:		SD-5102